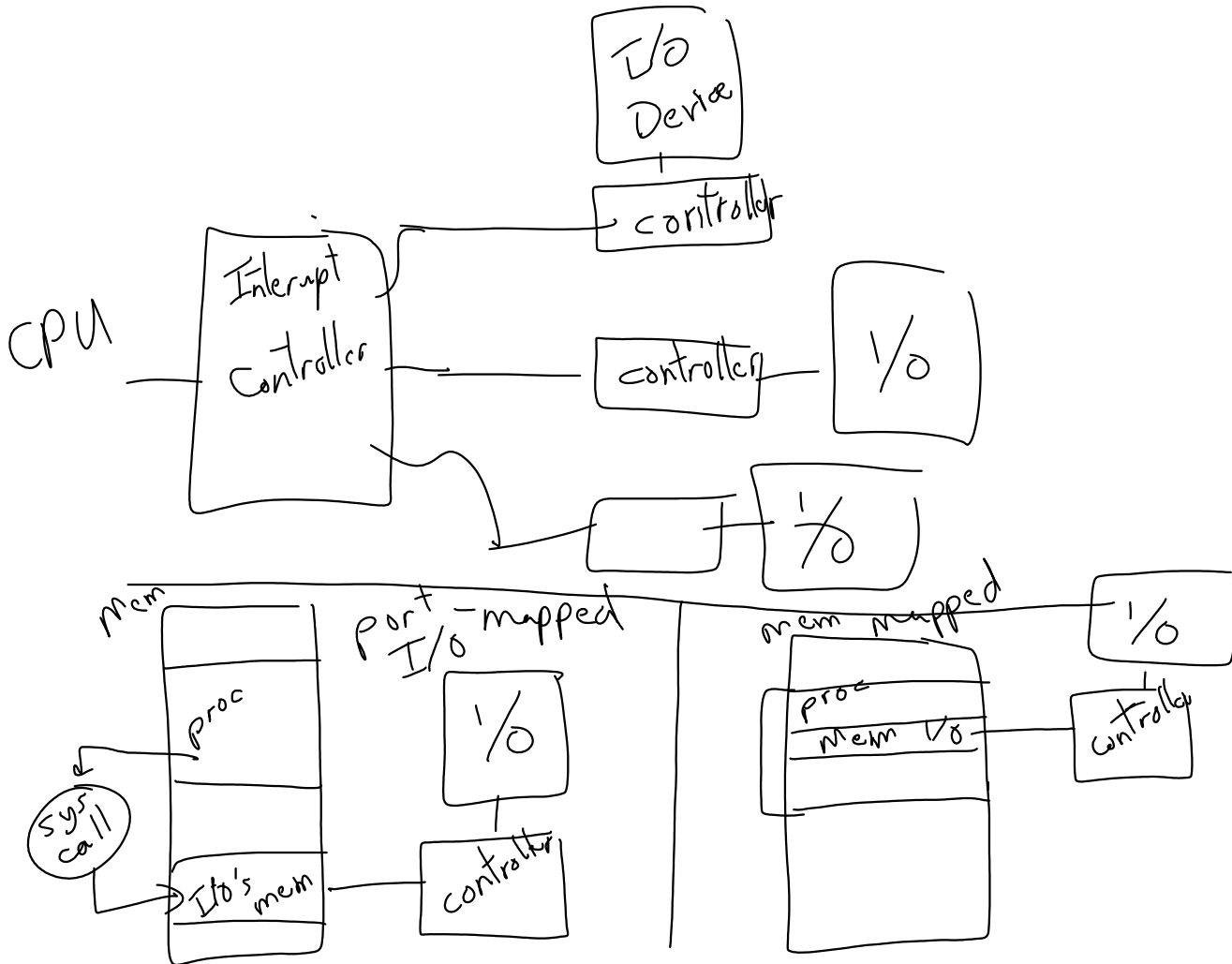
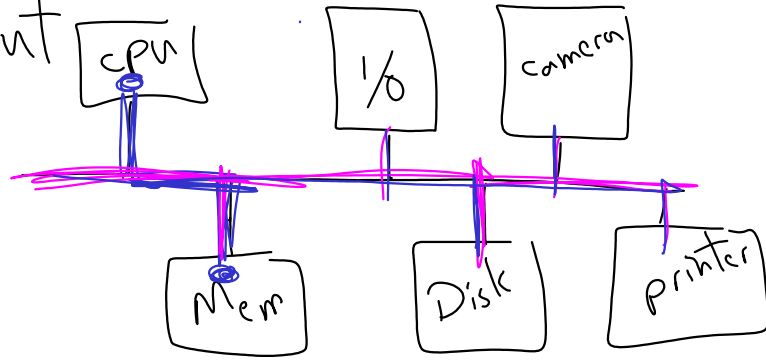
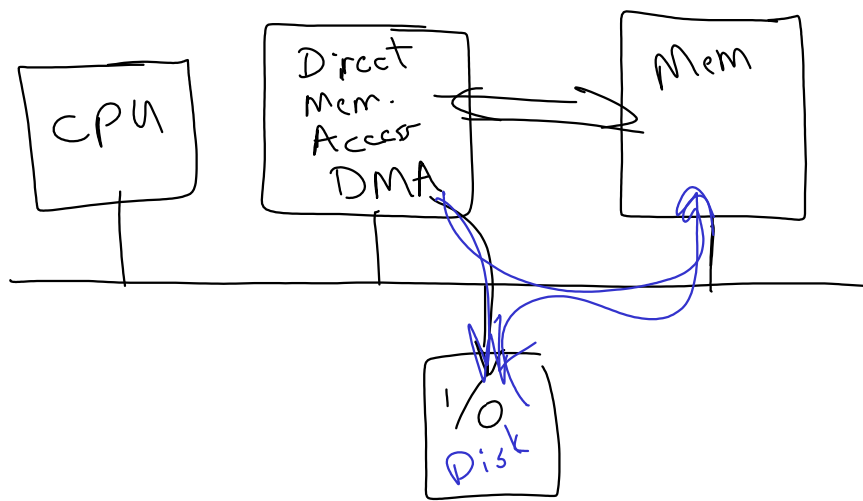
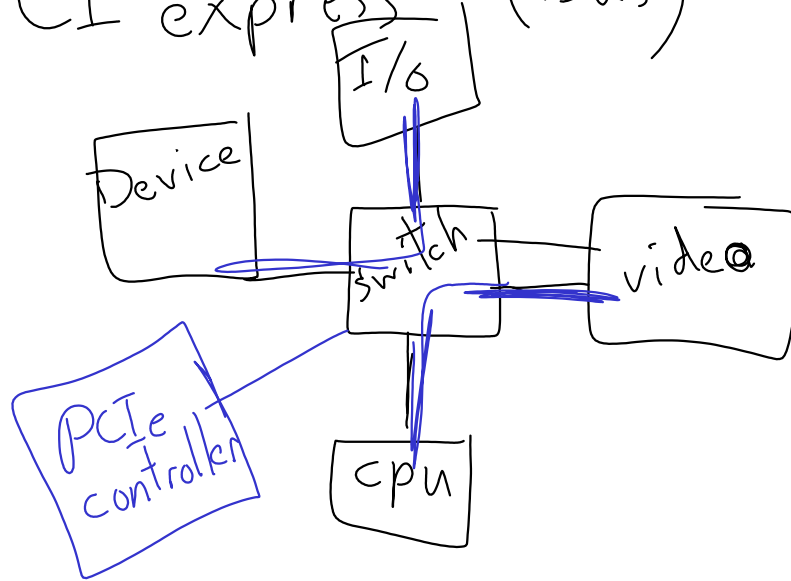


Input/Output BUS





PCI express (Bus)



Access (data/device)

Random - access any location
each access takes equal time
(memory)

Direct - access any location
- time is not the same for
all locations (disk)

Sequential - to access location n ,
must access $n-1$
(network, tape)

delay
seek: get to
the cylinder

rotational delay
wait for sector to
reach
~~move under~~ r/w
head

data transfer
sector
r/w head

